

## DETAILED ACTION

### *Allowable Subject Matter*

Claims 1-16 are allowed.

The following is an examiner's statement of reasons for allowance:

References Yonezawa et al (US 2003/0032213 A1), Kimura (US 2003/0052324 A1), Kumatani (US 2002/0149119 A1) and Guha et al (4,695,859) do not teach either in singularity or combination of the following claimed inventions:

- ❖ an antenna; an integrated circuit comprising a thin film transistor; a light-receiving element configured to receive a signal by optical communication; and
- ❖ a light-emitting element configured to transmit a signal by optical communication, wherein the light-receiving element has a layer for conducting photoelectric conversion using a non-single crystal thin film,
- ❖ wherein the light-emitting element has an electroluminescent layer using a non-single crystal thin film, wherein the integrated circuit includes a power supply circuit configured to generate a power supply voltage from an alternating voltage generated by the antenna, and
- ❖ wherein the antenna, the light-emitting element and the light-receiving element are electrically connected to the integrated circuit on the same substrate.

Art Unit: 2814

Yonezawa et al (US 2003/0032213 A1): Yonezawa et al (US 2003/0032213 A1)

does not explicitly disclose a light-emitting element configured to transmit a signal by optical communication, wherein the light-receiving element has a layer for conducting photoelectric conversion using a non-single crystal thin film, wherein the light-emitting element has an electroluminescent layer using a non- single crystal thin film, wherein the integrated circuit includes a power supply circuit configured to generate a power supply voltage from an alternating voltage generated by the antenna, and wherein the antenna, the light-emitting element and the light-receiving element are electrically connected to the integrated circuit on the same substrate.

Kimura (US 2003/0052324 A1): Kimura (US 2003/0052324 A1) does not explicitly disclose an antenna; an integrated circuit comprising a thin film transistor; a light-receiving element configured to receive a signal by optical communication; wherein the integrated circuit includes a power supply circuit configured to generate a power supply voltage from an alternating voltage generated by the antenna, and wherein the antenna, the light-emitting element and the light-receiving element are electrically connected to the integrated circuit on the same substrate.

Kumatani (US 2002/0149119 A1): Kumatani (US 2002/0149119 A1) does not explicitly disclose a light-emitting element configured to transmit a signal by optical communication, wherein the light-receiving element has a layer for conducting photoelectric conversion using a non-single crystal thin film, wherein the light-emitting

Art Unit: 2814

element has an electroluminescent layer using a non- single crystal thin film, wherein the integrated circuit includes a power supply circuit configured to generate a power supply voltage from an alternating voltage generated by the antenna, and wherein the antenna, the light-emitting element and the light-receiving element are electrically connected to the integrated circuit on the same substrate.

Guha et al (4,695,859): Guha et al (4,695,859) does not explicitly disclose an antenna; an integrated circuit comprising a thin film transistor; a light-receiving element configured to receive a signal by optical communication; wherein the integrated circuit includes a power supply circuit configured to generate a power supply voltage from an alternating voltage generated by the antenna, and wherein the antenna, the light-emitting element and the light-receiving element are electrically connected to the integrated circuit on the same substrate.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Art Unit: 2814

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to BILKIS JAHAN whose telephone number is (571)270-5022. The examiner can normally be reached on M-F, 8am-5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571)-272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Wai-Sing Louie/  
Primary Examiner, Art Unit 2814

BJ

Application/Control Number: 10/590,271

Page 6

Art Unit: 2814